

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q96021

Takeo HOSOMI, et al.

Group Art Unit: 2111

Appln. No.: 10/586,153

Examiner: Jeremy S CERULLO

Confirmation No.: 5617

Patent No.: 7,664,900

Filed: July 31, 2007

Issue Date: February 16, 2010

For: MULTIPROCESSOR SYSTEM AND METHOD FOR PROCESSING MEMORY ACCESS

REQUEST FOR CERTIFICATE OF CORRECTION

ATTN: Certificate of Correction Branch

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Pursuant to the provisions of 37 C.F.R. § 1.322, please enter the attached Certificate of Correction.

Since the errors noted are believed to be the fault of the Patent and Trademark Office, we are not enclosing the \$100.00 Certificate of Correction fee. If it is found to be to the contrary, please charge our Deposit Account No. 19-4880.

In view of the foregoing, issuance of the Certificate of Correction is respectfully requested.

Respectfully submitted,

/Howard L. Bernstein/

SUGHRUE MION, PLLC
Telephone: (650) 625-8100
Facsimile: (650) 625-8110

Howard L. Bernstein
Registration No. 25,665

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: July 13, 2010

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO: 7,664,900
DATED: February 16, 2010
INVENTOR(S): Takeo HOSOMI, Yoshiaki WATANABE

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 28, Delete "101-i, and" and insert --101-i,..., and--

Column 1, Line 31, Delete "(and n" and insert --(j and n--

Column 11, Line 65, Delete "each indicate Referring" and insert -- each indicate "o". ¶
Referring--

Column 30, Line 3, Delete "herein" and insert --wherein,--

MAILING ADDRESS OF SENDER:
SUGHRUE MION, PLLC

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

PATENT NO. 7,664,900

No. of additional copies